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Notice of Allowability	Application No.	Applicant(s)	
	10/709,784	CHAN ET AL.	
	Examiner	Art Unit	
	Don P. Le	2819	
	<u> </u>		
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate comi IGHTS. This application is	in this application. If not included nunication will be mailed in due cours	se. <b>THIS</b>
1. This communication is responsive to paper filed 8/20/04.	•		
2. The allowed claim(s) is/are <u>1-19</u> .			
3. The drawings filed on <u>5/27/04</u> are accepted by the Examin	er.		
<ul> <li>4. ☐ Acknowledgment is made of a claim for foreign priority ur</li> <li>a) ☐ All b) ☐ Some* c) ☐ None of the:</li> <li>1. ☐ Certified copies of the priority documents have</li> </ul>	e been received.		
2. Certified copies of the priority documents have	• •		
3. Copies of the certified copies of the priority do	cuments have been receiv	ed in this national stage application fr	om the
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		le a reply complying with the requiren	nents
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give	itted. Note the attached Exes reason(s) why the oath	KAMINER'S AMENDMENT or NOTIC or declaration is deficient.	E OF
6.  CORRECTED DRAWINGS ( as "replacement sheets") mus	st be submitted.		
(a) ☐ including changes required by the Notice of Draftspers	son's Patent Drawing Revi	ew ( PTO-948) attached	
1)  hereto or 2)  to Paper No./Mail Date			
(b) including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment	or in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1, each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on he header according to 37 (	the drawings in the front (not the back) FR 1.121(d).	of
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MA <sup>T</sup> FOR THE DEPOSIT OF B	TERIAL must be submitted. Note to the IOLOGICAL MATERIAL.	he
Attachmant(a)			
Attachment(s)  1.   Notice of References Cited (PTO-892)	5. Notice of	nformal Patent Application (PTO-152	)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. 🔲 Interview	<ul> <li>6. ☐ Interview Summary (PTO-413),</li> <li>Paper No./Mail Date</li> <li>7. ☐ Examiner's Amendment/Comment</li> </ul>	,
<ol> <li>Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 8/20/04</li> </ol>	8), 7. Examiner		
4.   Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner	s Statement of Reasons for Allowance	е
of Biological Material	9.	•	

## **Drawings**

1. Figures 1 and 2 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

## Allowable Subject Matter

- 2. Claims 1-19 are allowed.
- 3. The following is an examiner's statement of reasons for allowance:

With respect to claims 1, 6 and 13, in addition to other elements in the respective claim, the prior art does not teach a logic circuit comprising a first inverter having linear gate transistors with a first input capacitance and a second inverter having cellular gate transistors with a second input capacitance connected in series with the first inverter.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Don P. Le whose telephone number is 571-272-1806. The examiner can normally be reached on 7AM - 5PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael J. Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

5/10/2005

DON LE
PRIMARY EXAMINER